Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.38$ V at $I_F = 5$ A

TO-263AB K VB40100C

TMBS[®]

PRIMARY CHARACTERISTICS					
Package	TO-263AB				
I _{F(AV)} 2 x 20 A					
V _{RRM}	100 V				
I _{FSM}	250 A				
V_F at $I_F = 20$ A	0.61 V				
T _J max.	150 °C				
Diode variations	Common cathode				

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Low thermal resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C

VB40100C-M3, VB40100CHM3

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- AEC-Q101 qualified available:
 - Automotive ordering code P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

MECHANICAL DATA

Case: TO-263AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	VB40100C	UNIT		
Maximum repetitive peak reverse voltage		V _{RRM}	100	V		
Maximum average forward rectified current (fig. 1)	per device		40			
	per diode	IF(AV)	20	— A		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I _{FSM}	250	А		
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs		
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +150	°C		



RoHS

COMPLIANT HALOGEN



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F	0.47	-	V	
	I _F = 10 A			0.54	-		
	I _F = 20 A			0.67	0.73		
	I _F = 5 A	T _A = 125 °C		0.38	-		
	I _F = 10 A			0.45	-		
	I _F = 20 A			0.61	0.67		
Reverse current at rated V_R per diode ⁽²⁾	V _R = 70 V	T _A = 25 °C	I _R	9	-	μA	
		T _A = 125 °C		10	-	mA	
	V= = 100 V	T _A = 25 °C		-	1000	μA	
	V _R = 100 V	T _A = 125 °C		21	45	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL VB40100C		UNIT	
Typical thermal resistance per diode	$R_{ ext{ heta}JC}$	2.0	°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VB40100C-M3/4W	1.39	4W	50/tube	Tube	
TO-263AB	VB40100C-M3/8W	1.39	8W	800/reel	Tape and reel	
TO-263AB	VB40100CHM3/I (1)	1.39	l	800/reel	Tape and reel	

Note

(1) AEC-Q101 qualified



VB40100C-M3, VB40100CHM3

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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

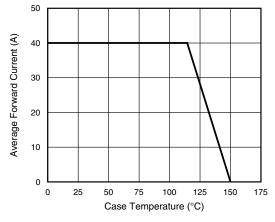


Fig. 1 - Forward Current Derating Curve

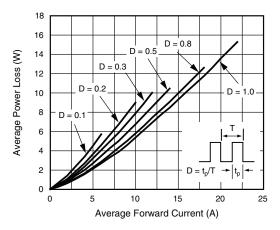


Fig. 2 - Forward Power Loss Characteristics Per Diode

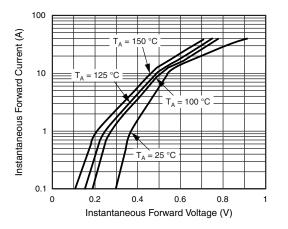


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

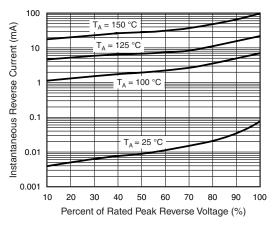


Fig. 4 - Typical Reverse Characteristics Per Diode

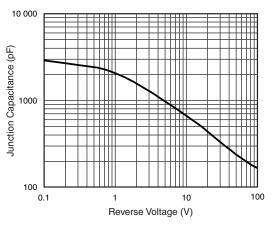


Fig. 5 - Typical Junction Capacitance Per Diode

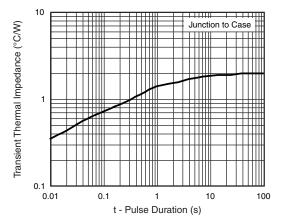


Fig. 6 - Typical Transient Thermal Impedance Per Diode

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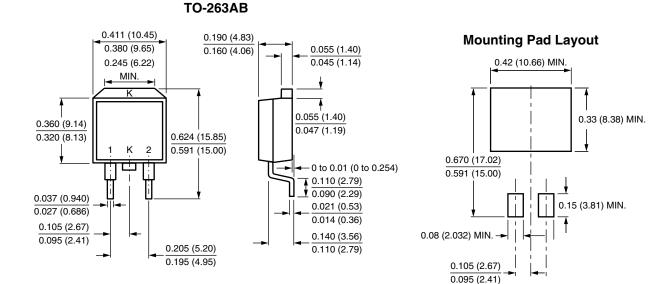
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VB40100C-M3, VB40100CHM3

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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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